

Motivation and Objectives

- Current technology in electronics manufacturing includes uses of harmful substrates contributing to the environmental pollution.
- Electronic manufacturing produces huge amount of volatile organic compound and carbon deposit to the surrounding every year.
- It has become a necessity to introduce eco-friendly and efficient alternative solution for producing electronics system in order to get sustainable manufacturing.
- Using paper substrate embedded with seed instead of traditional plastic polymeric substrates can decrease the huge production of carbon deposit every year.
- Flexible electronics built with seed paper can contribute to reducing the decomposition period after the life cycle of electronics which currently takes excessive time because of traditional substrates.
- Printing with water-based ink instead of solvent-based ink for circuits will reduce the VOC production.
- Use seed paper to print test vehicle with water-based sustainable ink.
- To do a sintering study based on suitable sintering time and temperature and cure it for good conductive resistance.
- Use of electrically conductive adhesive (ECA) material for component attachment.
- Testing HTOL performance of the traces and SMDs.
- Testing performance of the functional circuit.

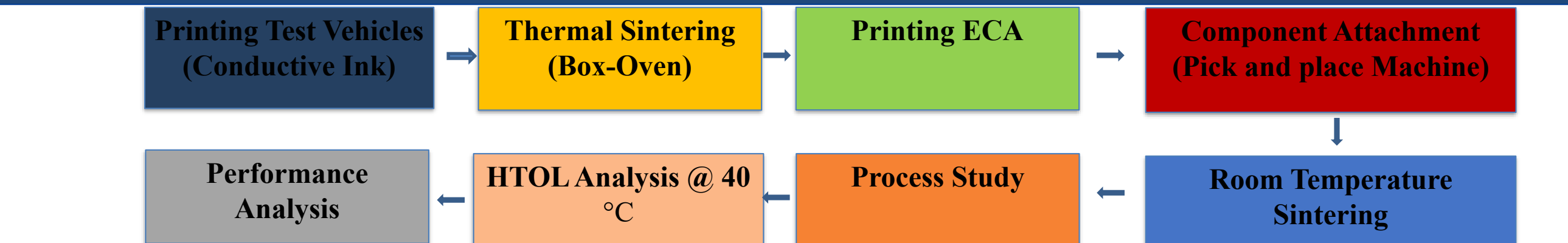
Print Parameters and Properties



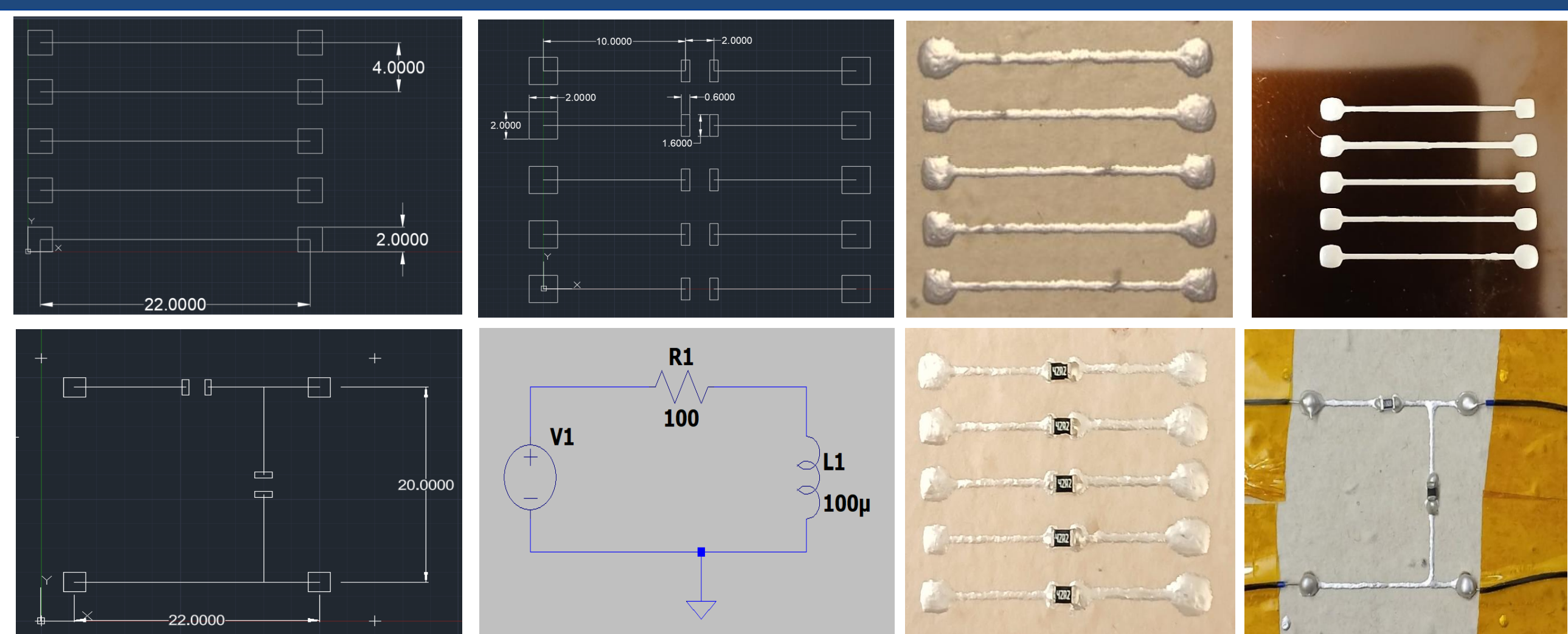
Print Platform

Print Parameters		Ink properties		Substrate-1 Properties	
Nozzle ID & OD	159 & 244 μm	Silver Content	75 wt%	Material	Seed Paper
Ink Pressure	15 Psi	Viscosity @ 0.1s^{-1}	26000 cps	Blend Name	Wildflower seed paper blend
Print Speed	3 mm/s	Viscosity @ 100s^{-1}	800 cps	Thickness	16.5 mil
No. of Passes	1	Specific Gravity	3.1	No. of types of seed	6
Standoff Height	50 μm	Flash Point	Non-flammable		
		Adhesive properties		Substrate-2 Properties	
		Hardness, Shore D	85	Material	Polyamide Sheet
		Volume Resistivity	$<0.0001 \Omega\cdot\text{cm}$	Name	Kapton® HN
		CTE	$1.5 \times 10^{-5} / ^\circ\text{C}$	Density	1.42 g/cc
		Cure Time	24 hrs, 45 min, 15 min	Thickness	5 mil
		Cure Temperature	Room Temp., 50°C , 100°C	Operating Temperature	-269°C to 400°C
				CTE(100°C to 200°C)	32 ppm/ $^\circ\text{C}$

Workflow

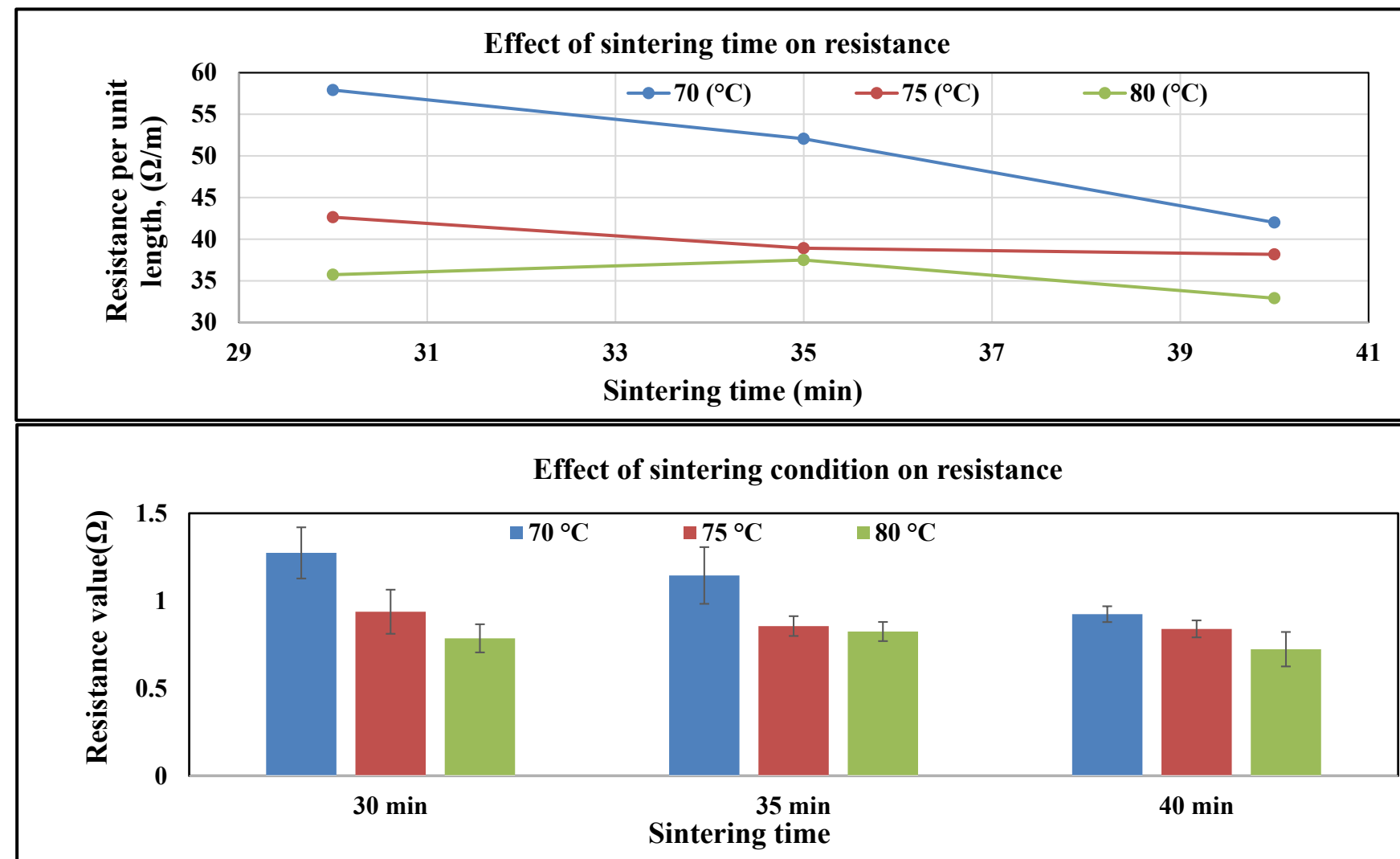


Test Vehicles

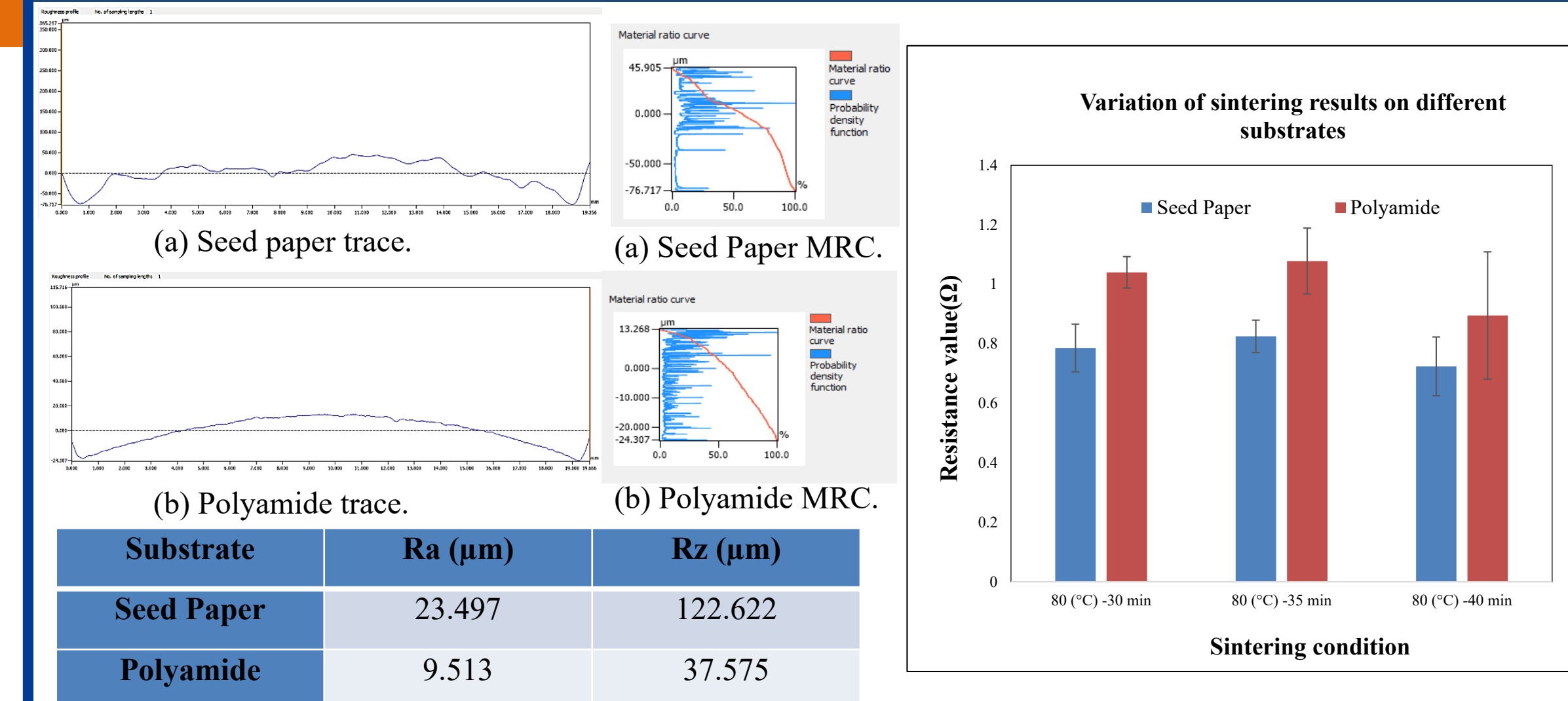


Test Results

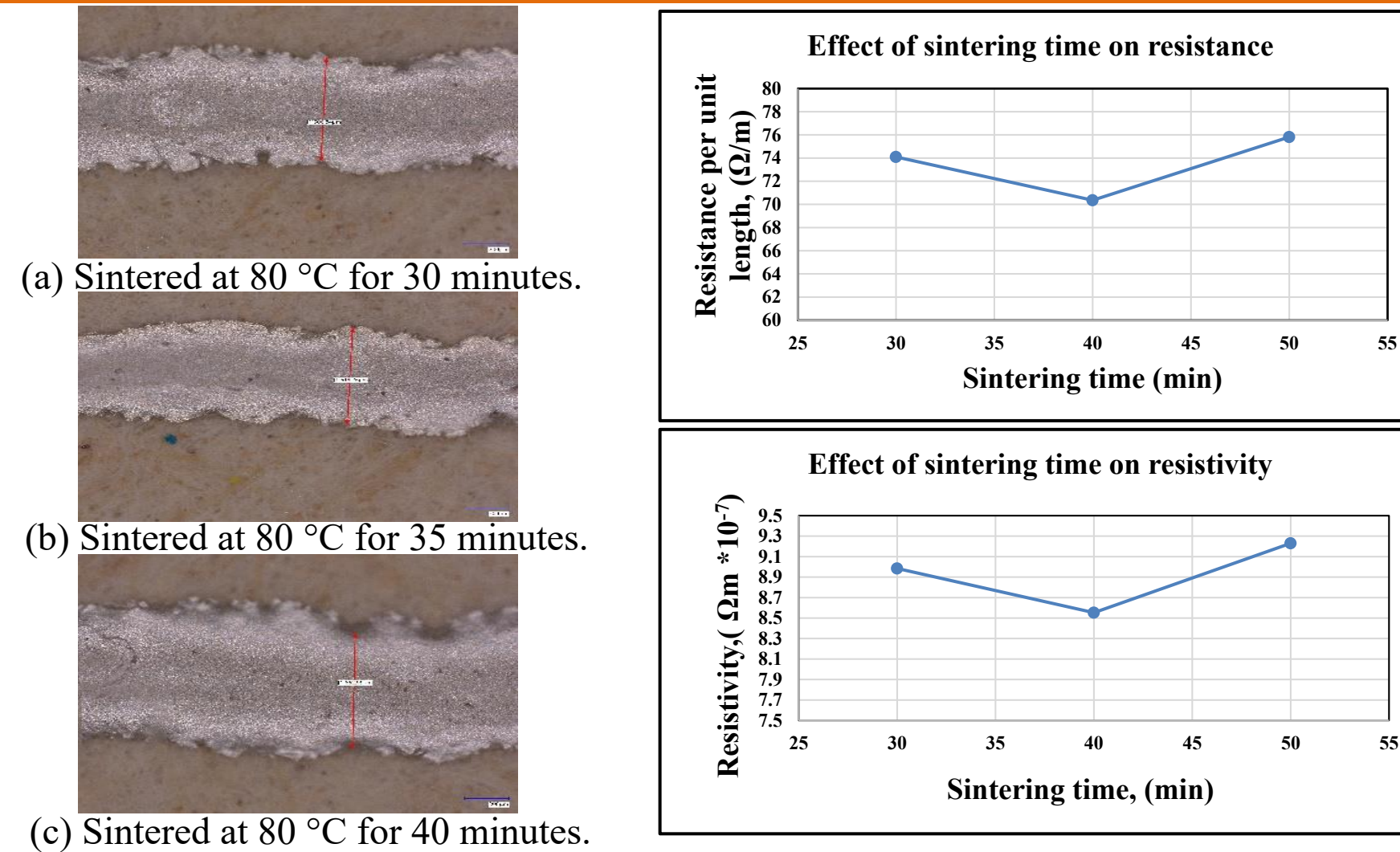
Effect of Sintering Temperature on Resistance



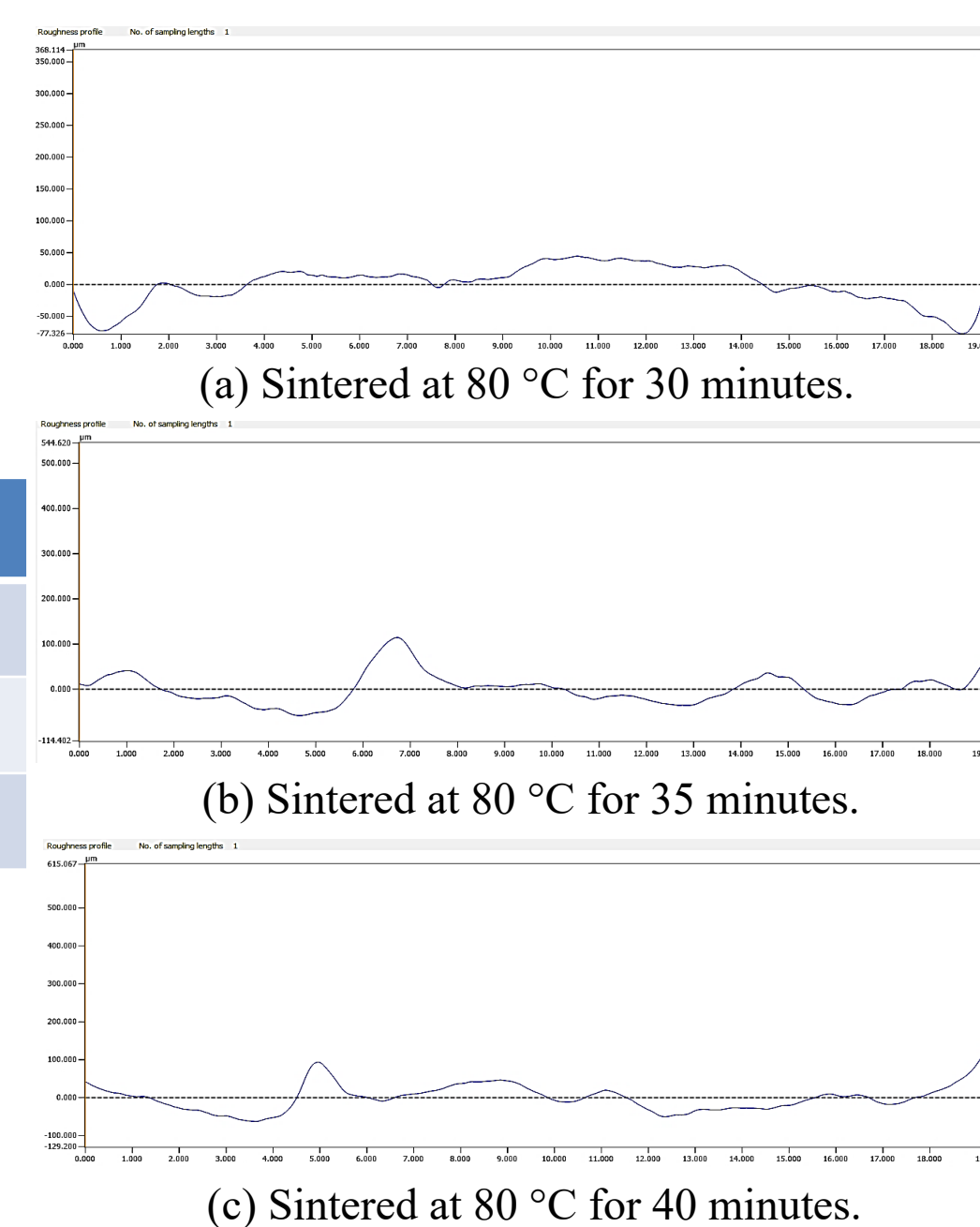
Result Comparison with Polyamide



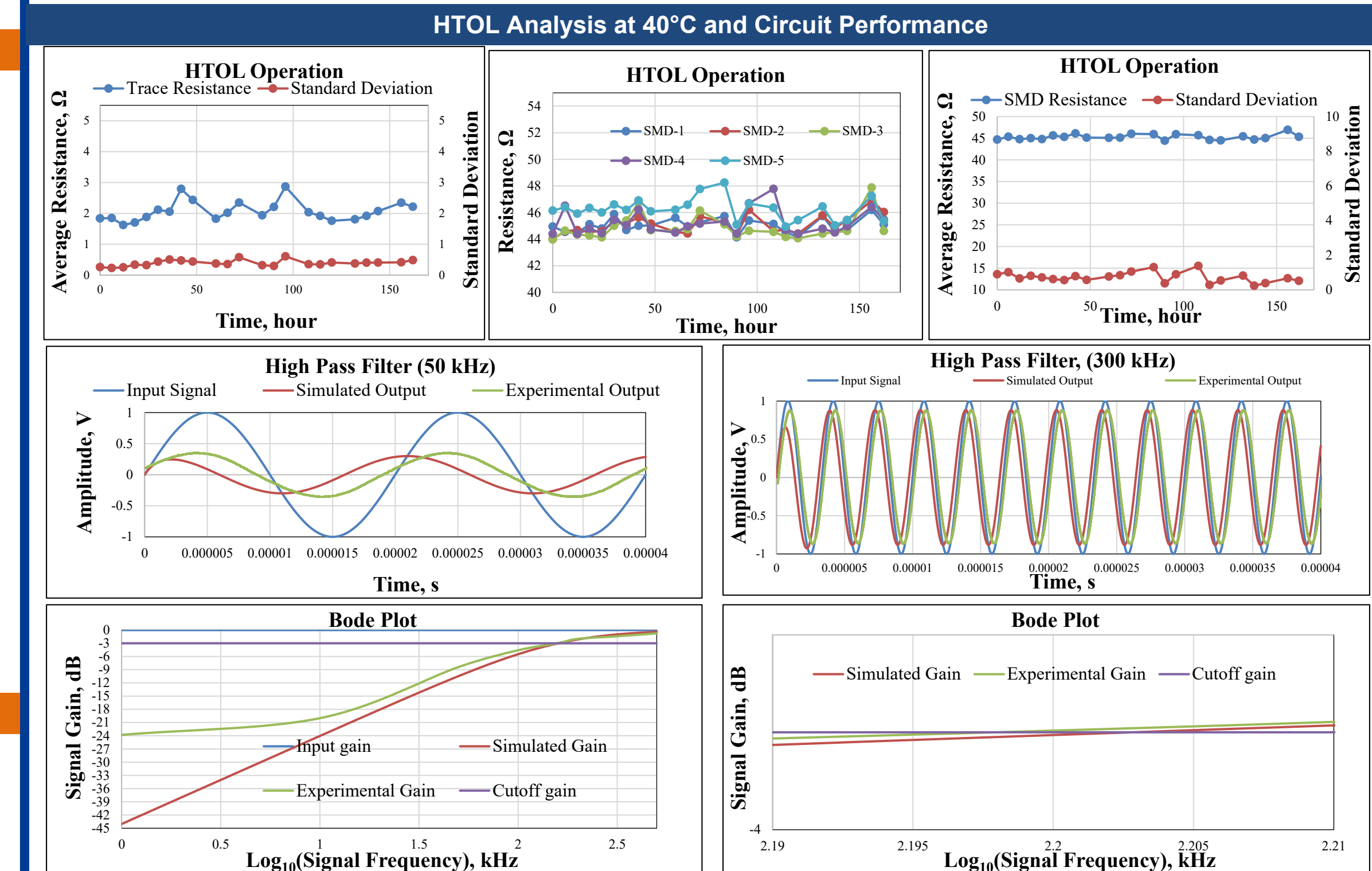
Effect of Sintering Time on Resistance and Resistivity @ 80 °C



Trace Roughness Profile



Time (min)	Ra (μm)	Rz (μm)
30	24.243	121.943
35	25.522	173.026
40	26.670	192.105



Summary and Conclusion

- In this study,
- Seed paper has been investigated as an alternative to the traditional printing substrate.
 - Process study on sintering time and temperature has been done to find out the optimum condition for best possible conductive traces.
 - Electrically conductive adhesive has been applied to do SMD attachment.
 - HTOL operation has been investigated on the traces and SMDs till 162 hours.
 - Circuit performance of High Pass filter has been analyzed.
 - Seed paper-based electronics can be a next generation solution for sustainable electronics in additive manufacturing.

Acknowledgment

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